

FIG. 2

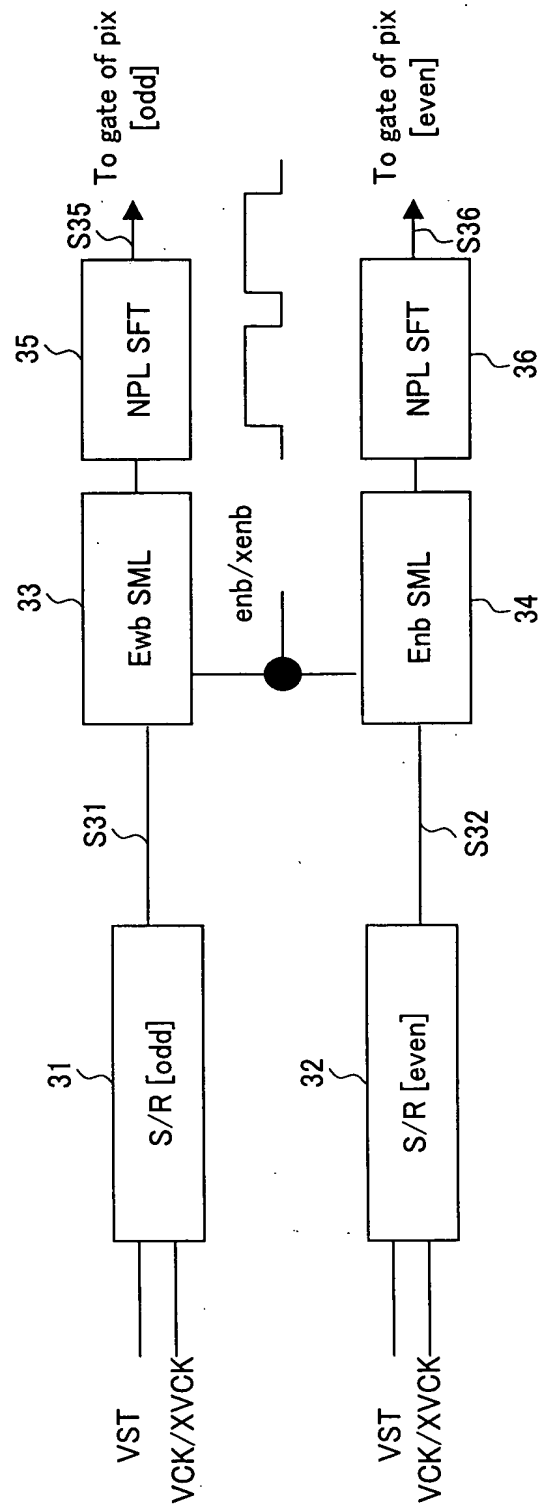


FIG. 3A VCOM



FIG. 3B VCK



FIG. 3C S31



FIG. 3D S32

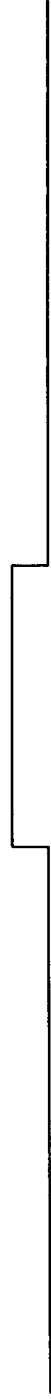


FIG. 3E S35



FIG. 3F S36



FIG. 4

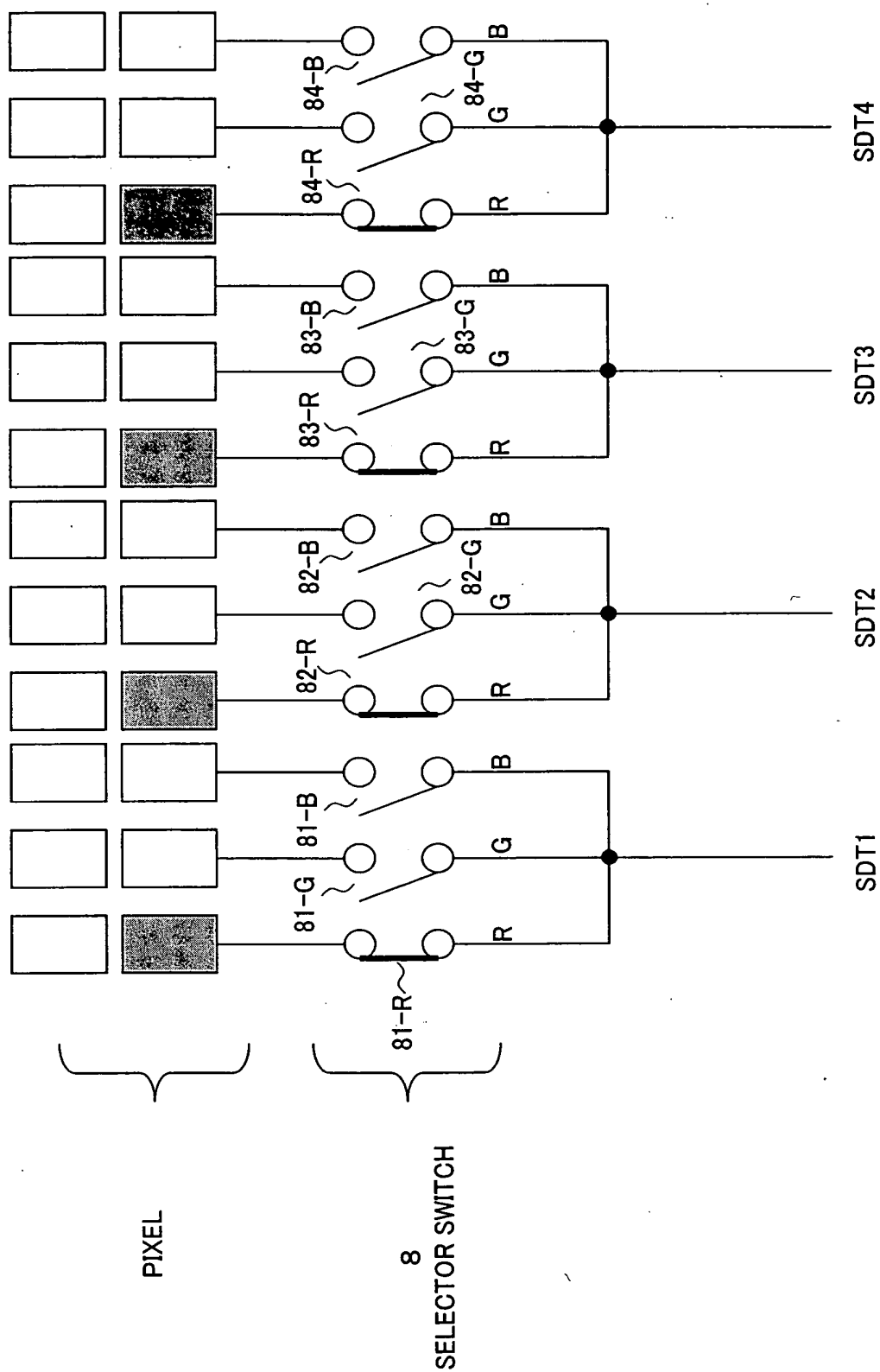


FIG. 5

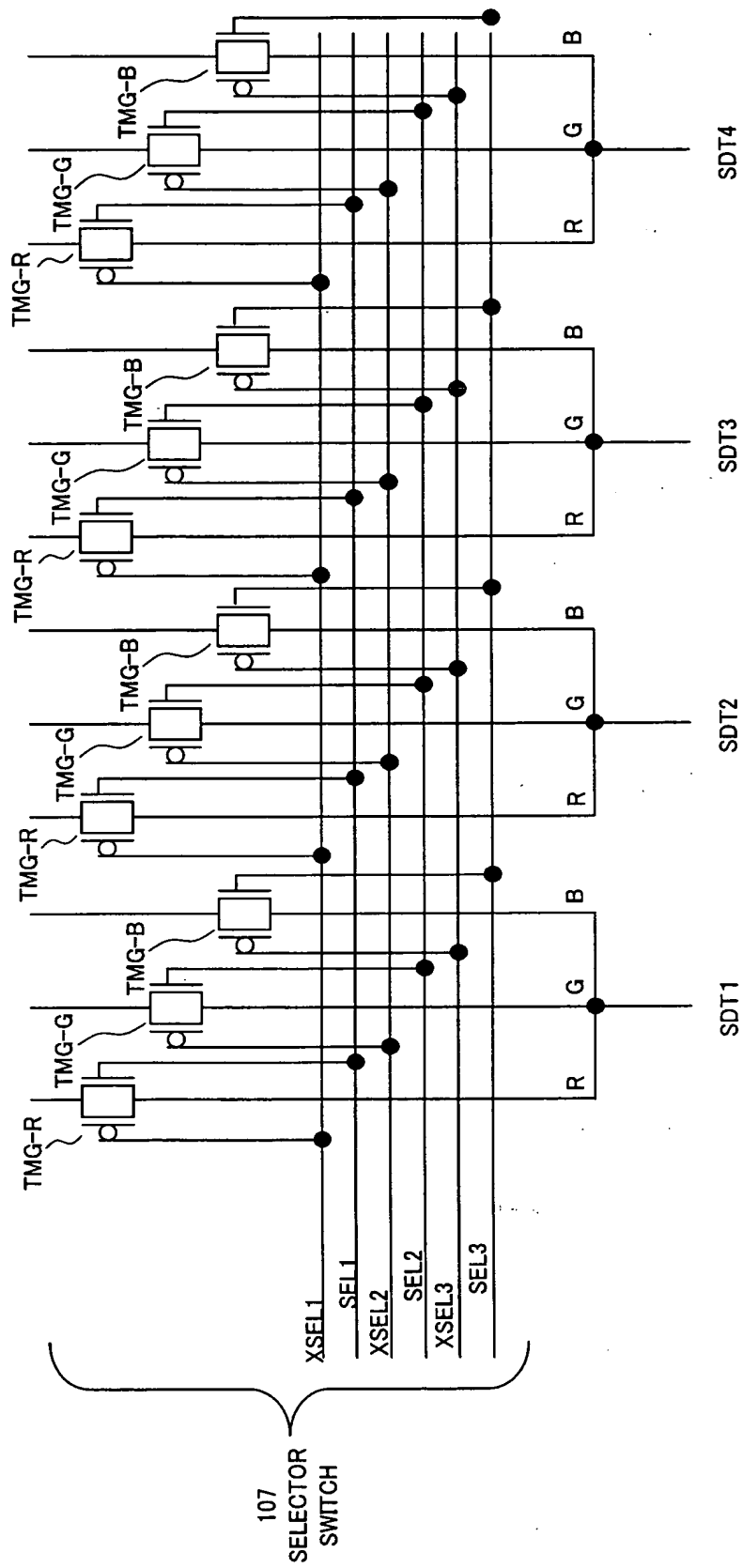


FIG. 6

9

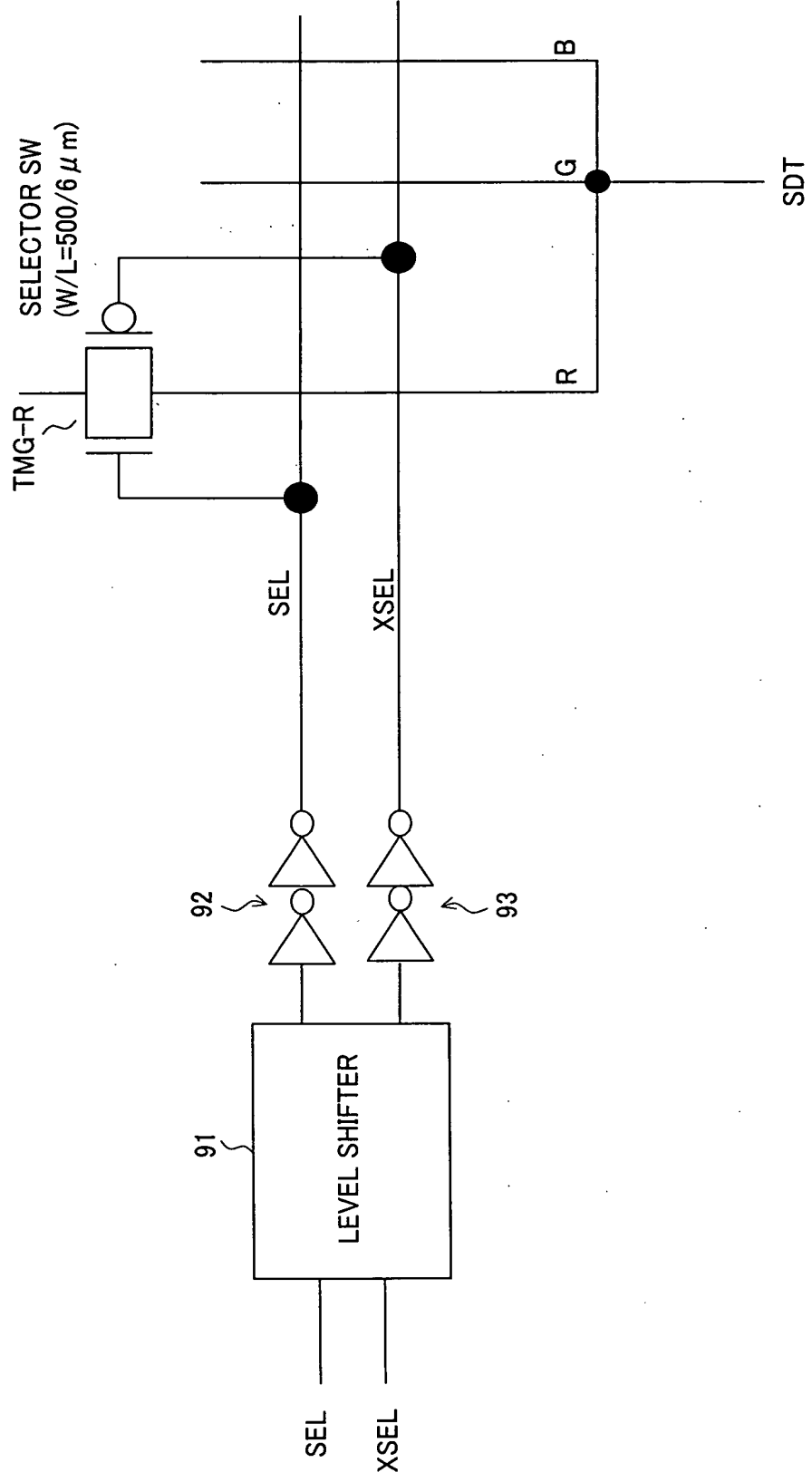


FIG. 7

100

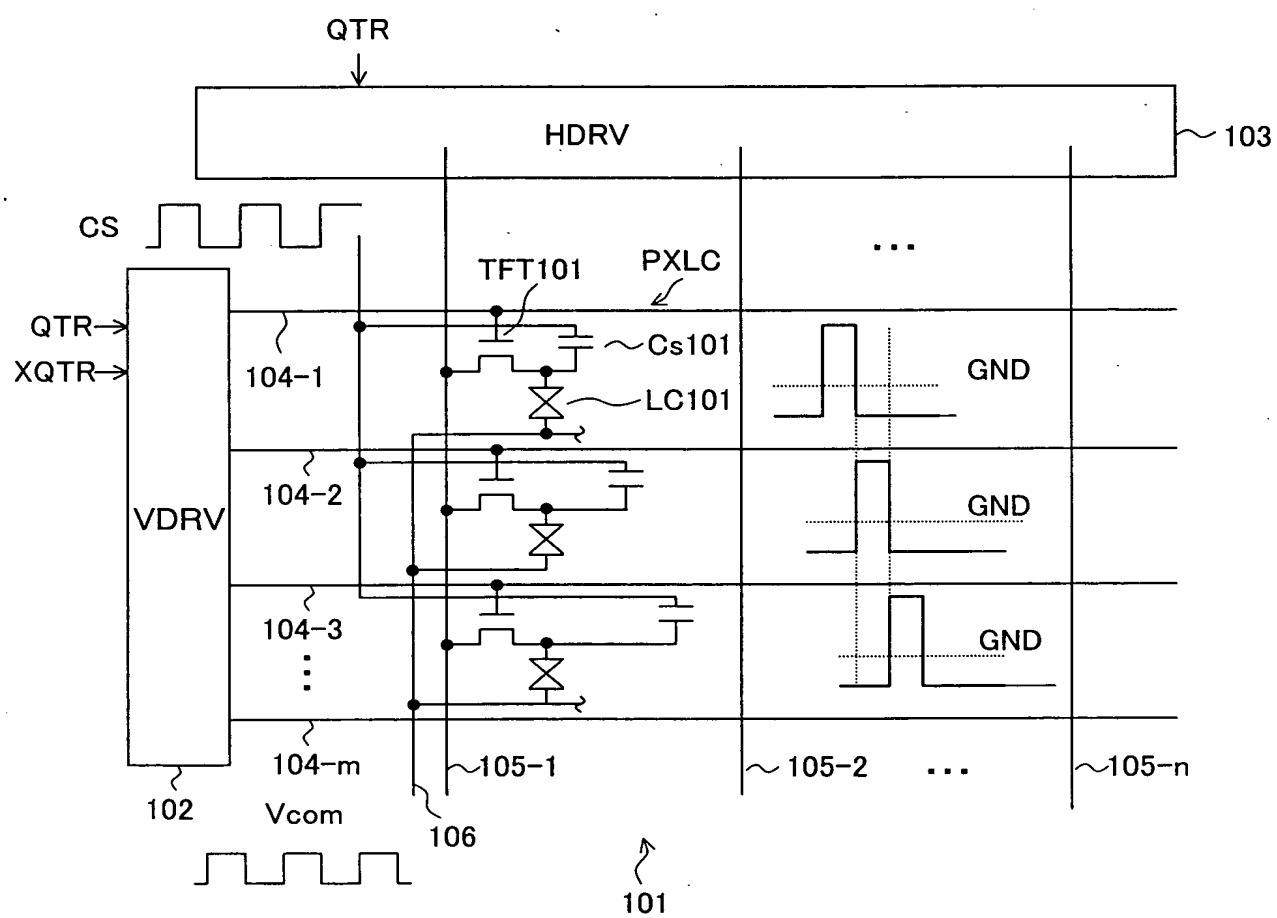


FIG. 8A

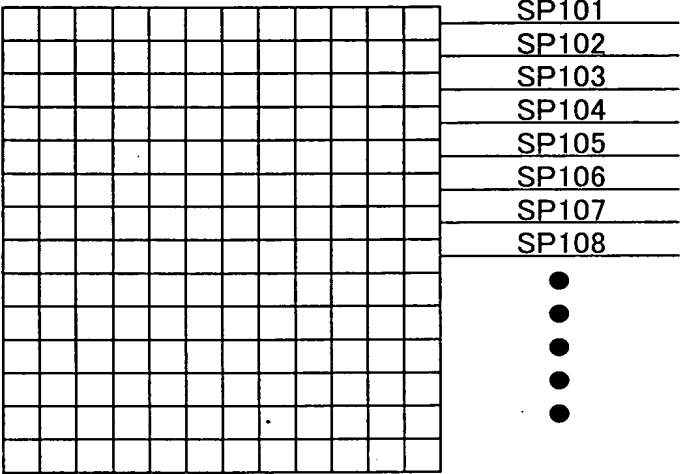


FIG. 9A

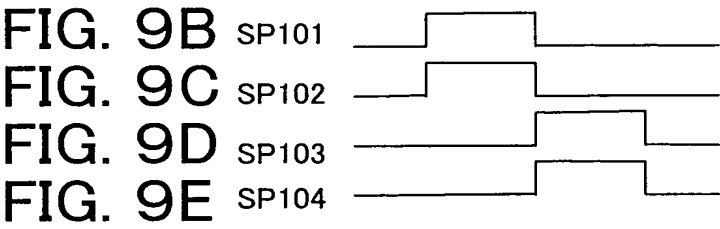
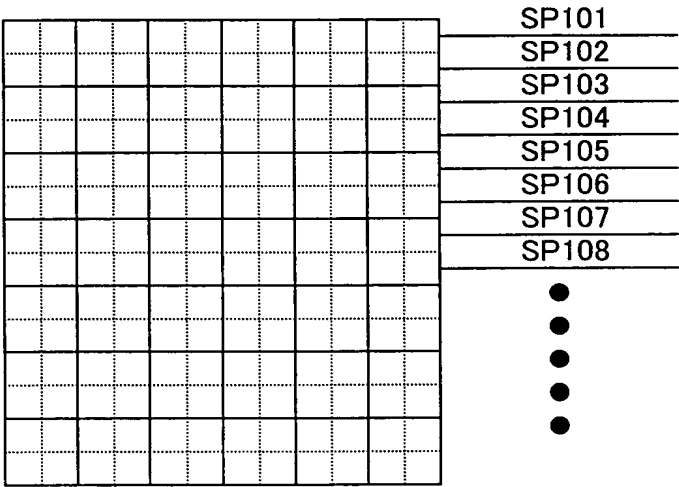


FIG. 10

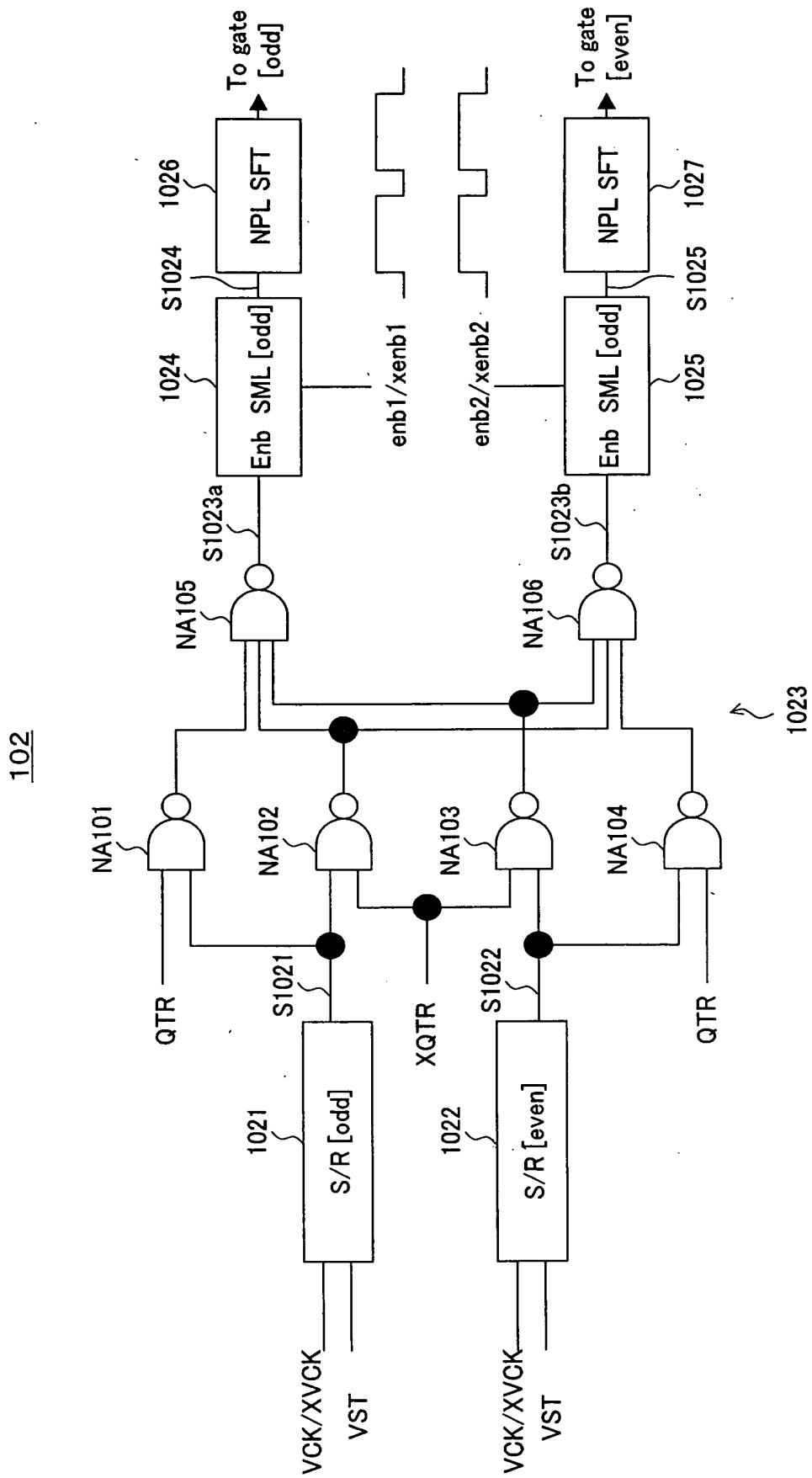


FIG. 11

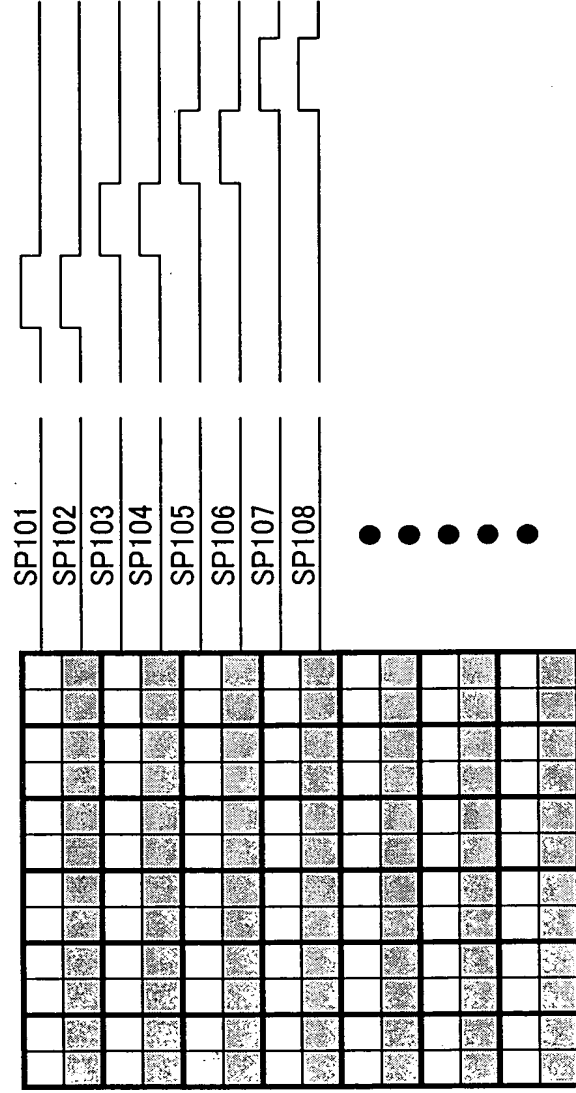


FIG. 12

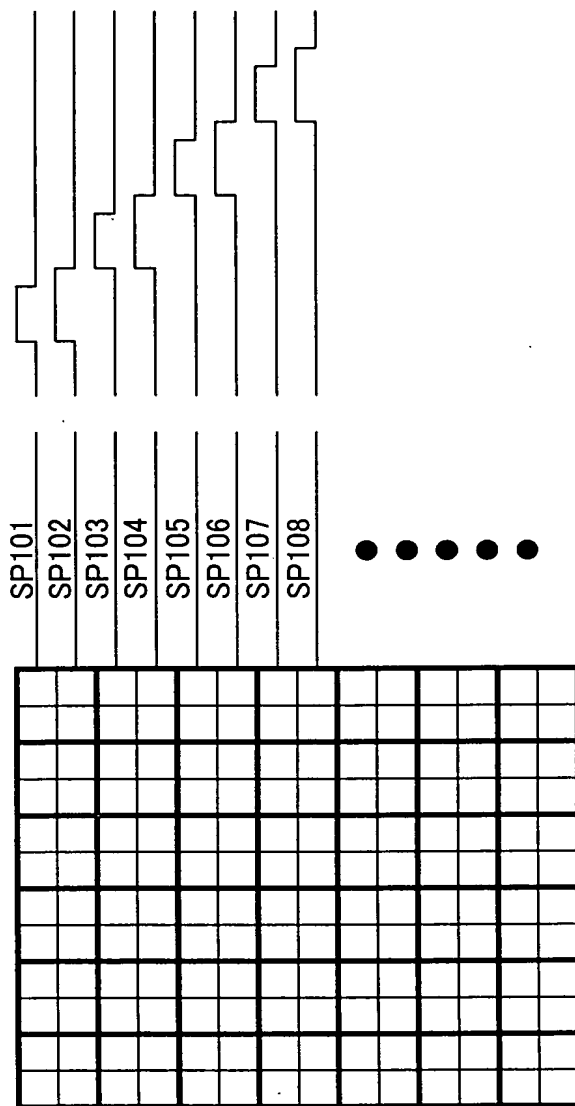


FIG. 13

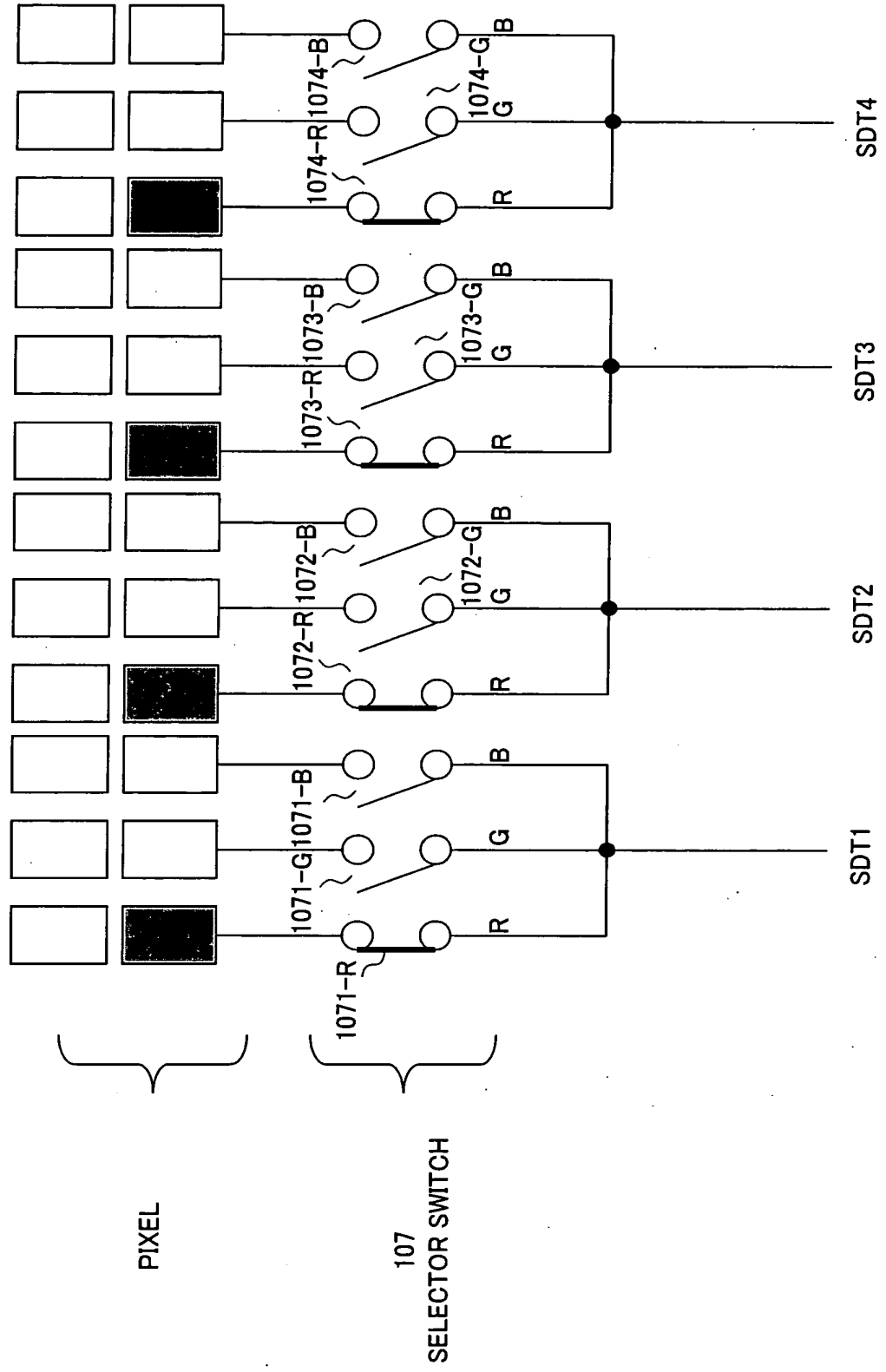


FIG. 14

108

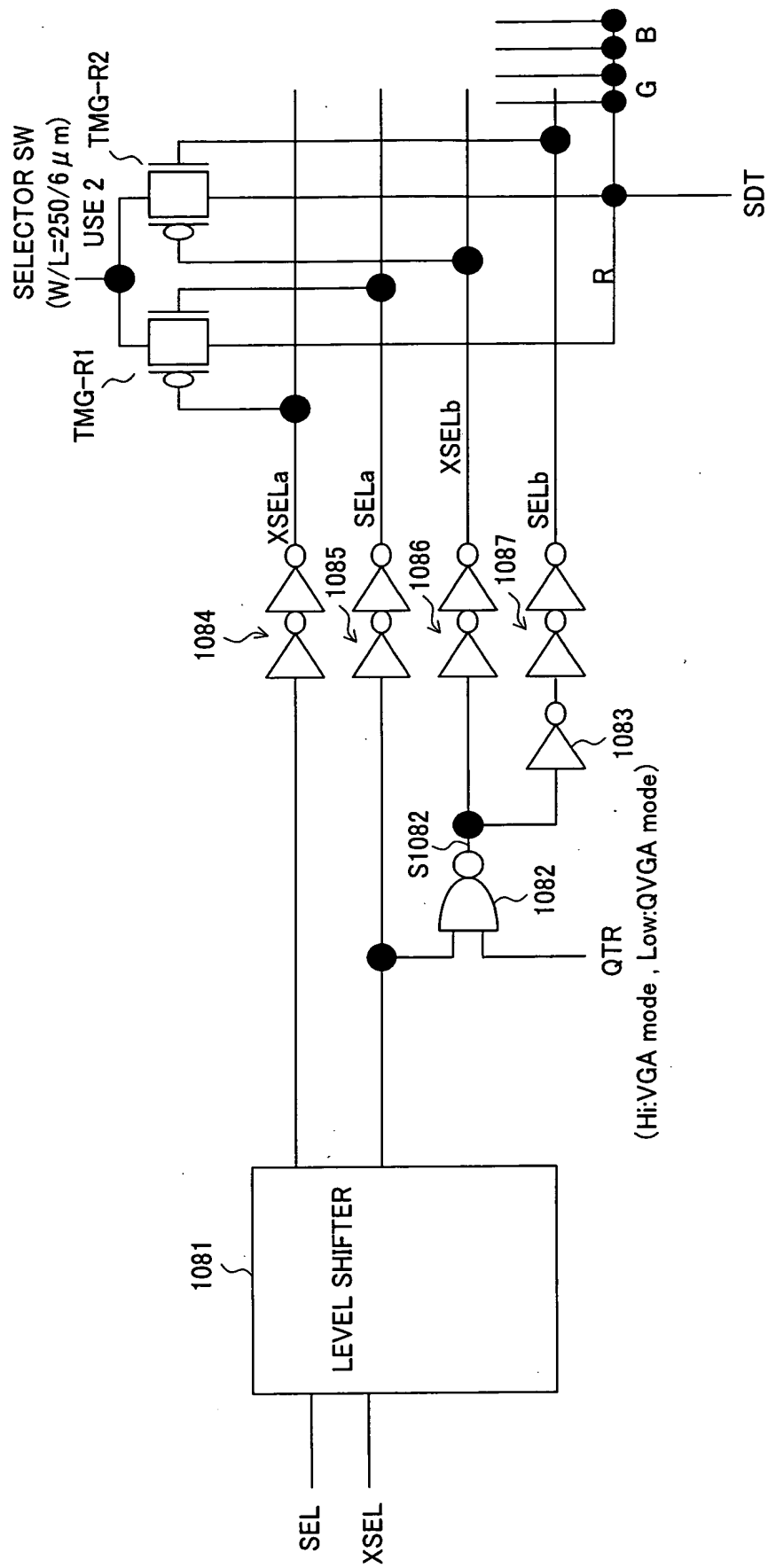
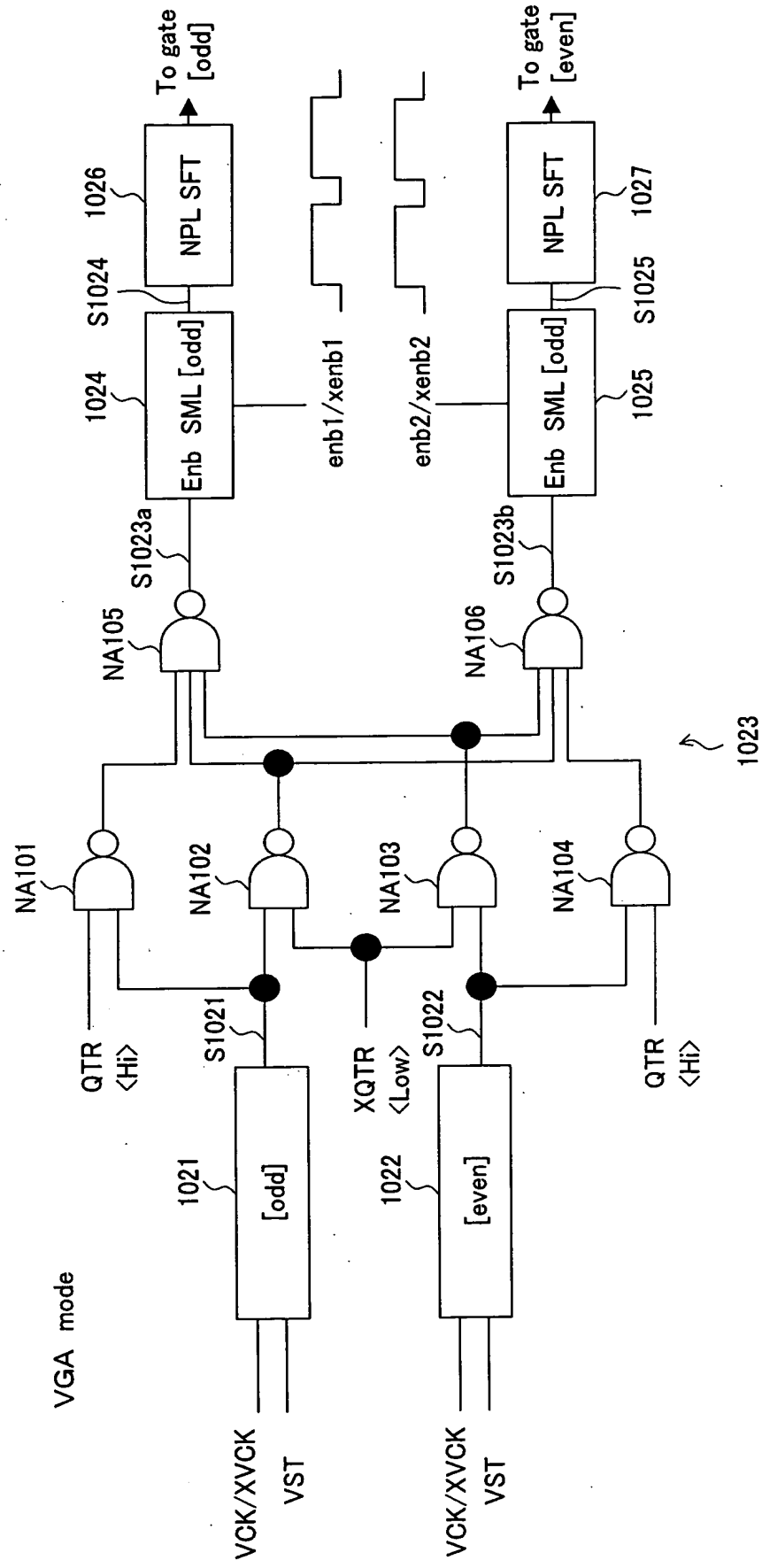


FIG. 15



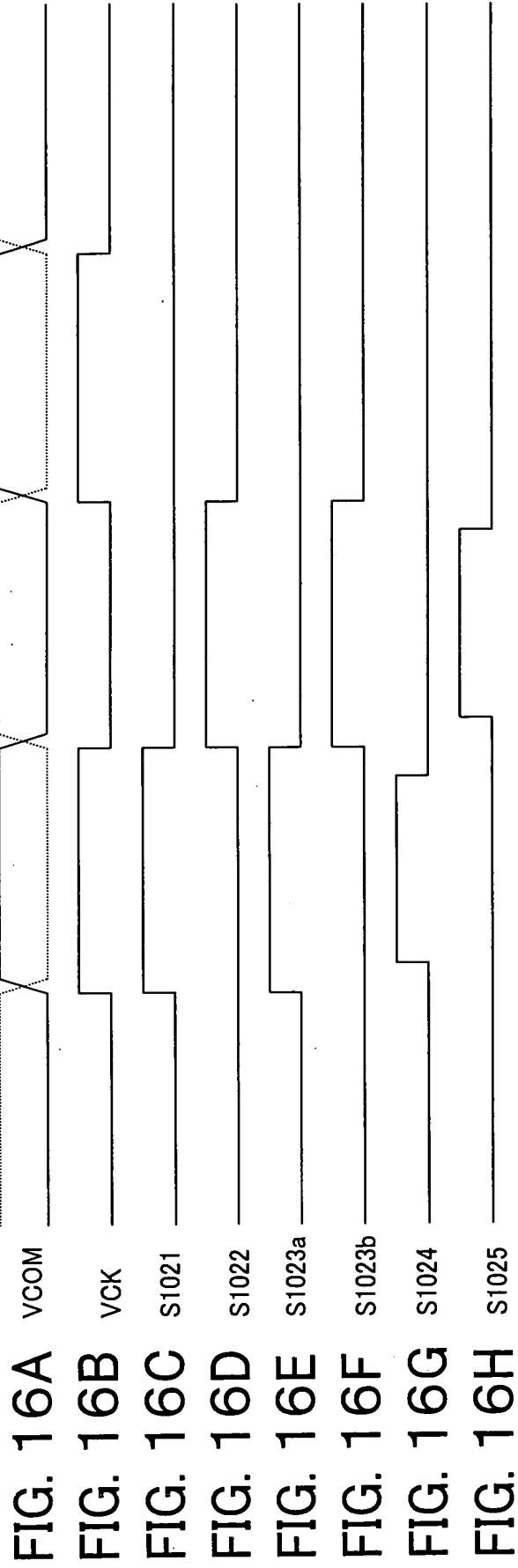


FIG. 17

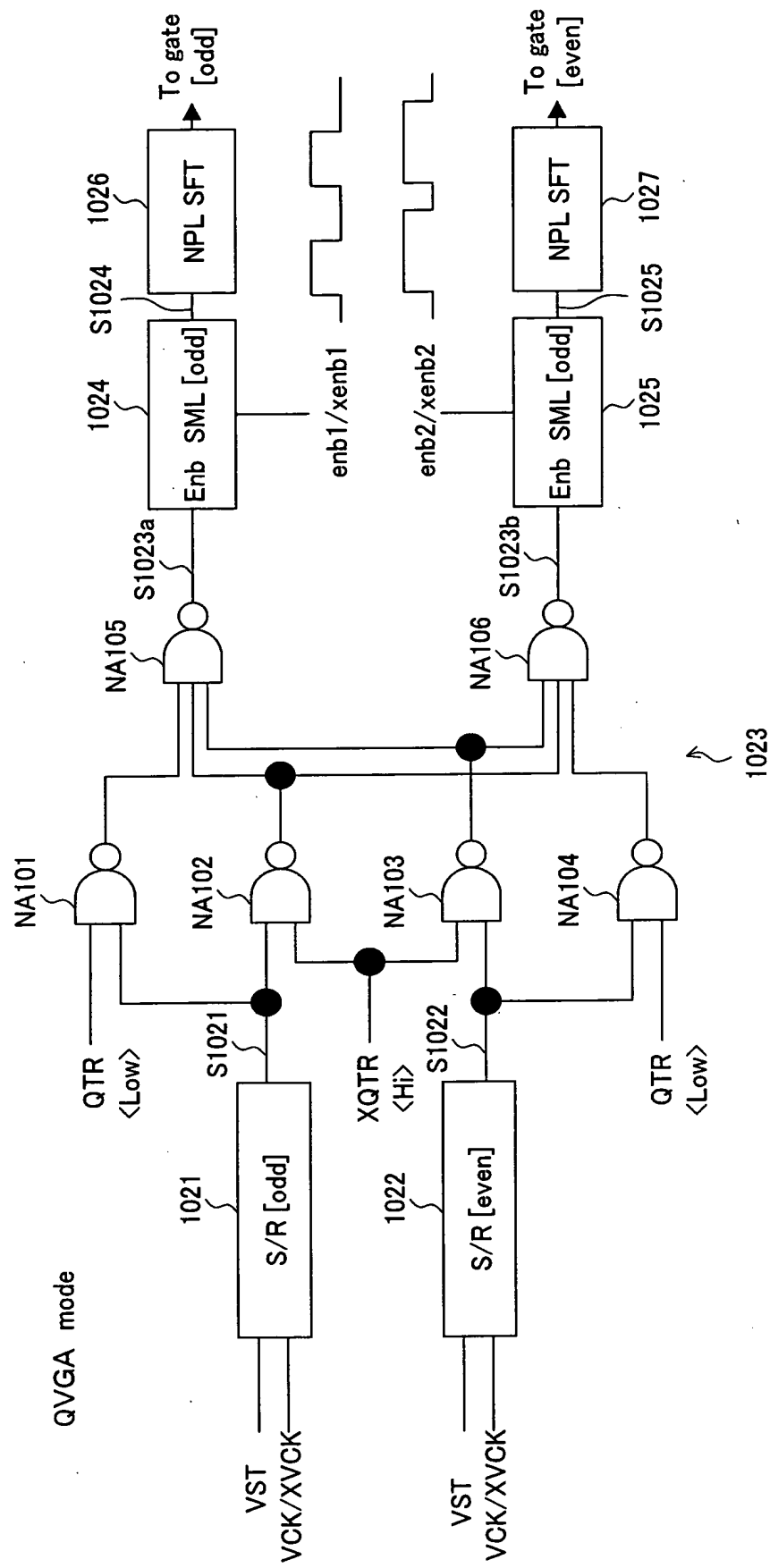


FIG. 18A



FIG. 18B



FIG. 18C



FIG. 18D



FIG. 18E

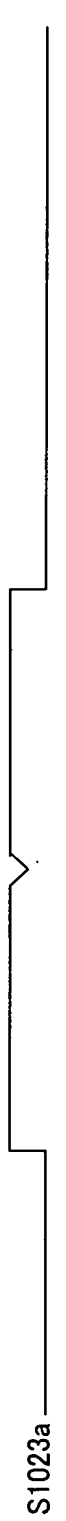


FIG. 18F



FIG. 18G



FIG. 18H



FIG. 19

		VGA MODE	QVGA MODE	
			Ref CIRCUIT	LOW CONSUMPTION USE CIRCUIT
SELECTOR SW Tr SIZE	W	500 μ m	500 μ m	250 μ m(+250 μ m)
	L	6 μ m	6 μ m	6 μ m
CONSUMED POWER		8.5mW	4.25mW	2.13mW
SIGNAL LINE WRITING τ		0.88 μ sec	0.88 μ sec	1.0 μ sec
SELECTOR FALLING τ		0.16 μ sec	0.16 μ sec	0.16 μ sec

FIG. 20

